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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10078284	FILING DATE 02/15/2002	CLASS 438	SUBCLASS 637	GAU 2842	2823	EXAMINER ESTRADA
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**APPLICANTS: Vanhaelemeersch Serge; Maex Karen;

**CONTINUING DATA VERIFIED:

THIS APPLN CLAIMS BENEFIT OF 60/269,109 02/15/2001

** FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials		ATTORNEY DOCKET NO IMEC238.001AUS
TITLE : Method of fabricating a semiconductor device		

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
Amount Due Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		Print Fig.	
Primary Examiner PREPARED FOR ISSUE		Application Examiner	
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